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(12)

#### **EUROPEAN PATENT APPLICATION**

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01.02.1999 JP 2338399 01.02.1999 JP 2338499 01.02.1999 JP 2338299 21.10.1999 JP 29918899 (71) Applicant: SONY CORPORATION Tokyo (JP)

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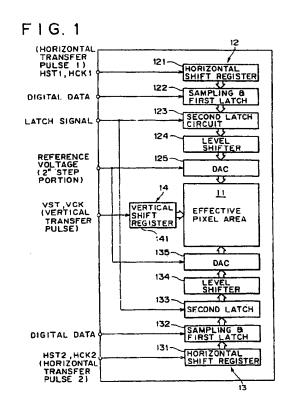
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### (54) Data driver comprising a digital/analog converter for a liquid crystal display device

(57)A drive circuit (12, 13) integrated with the LCD device containing a digital/analog converter circuit (125, 135) has polysilicon thin film transistors arrayed in a matrix on the substrate as switching devices for the pixels. A level shift circuit (124, 134) in the shift register has a basic structure of CMOS latch cells and is utilized in each level shift of the clock signal at each transfer stage. A sampling latch circuit (122, 132) with a basic structure of CMOS latch cells has a level shift function. These respective circuits may be incorporated into a single scanning type structural circuit with the drive circuit-integrated liquid crystal display device to provide an LCD panel with an extremely narrow picture frame, stable level shift operation, stable sampling & latch operation in a circuit structure having an extremely small number of components, low power consumption and a small surface area.



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## **EUROPEAN SEARCH REPORT**

Application Number

EP 99 40 3241

Category	Citation of document with i	ndication, where appropriate.	Relevant	CI ASSISICATION OF THE
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	The present search report has	been drawn up for all claims	1	
	Place of search	Date of completion of the search	1	Examiner
	THE HAGUE	3 June 2002	Lan	ge, J
X : parti Y : parti docu A : tech	ATEGORY OF CITED DOCUMENTS icularly relevant if taken alone icularly relevant if combined with another in the same category notogical background—witten disclosure	E.: earlier patent do	e underlying the cument, but public in the application or other reasons	invention Shed on, or



Application Number

EP 99 40 3241

CLAIMS INCURRING FEES
The present European patent application comprised at the time of tiling more than ten claims.
Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims tees have been paid, namely claim(s):
No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.
LACK OF UNITY OF INVENTION
The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:
see sheet B
All turther search tees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.
Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:
None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:



### **EUROPEAN SEARCH REPORT**

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X : par Y : par doc A : tecl O : nor	CATEGORY OF CITED DOCUMENTS  Ilcularly relevant if taken alone licularly relevant if combined with and ument of the same category inological background written disclosure irmediate document	T: theory or pr E: earlier pate after the fill ther D: document 1. document	inciple underlying the nt document, but put ng date tited in the applicatio #ed for other reason	e invention on, or n s



# LACK OF UNITY OF INVENTION SHEET B

Application Number

EP 99 40 3241

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims: 1-16

decoder for D/A converter

2. Claims: 17-33

level shifter using voltage divider and having CMOS latch cell as basic structure

3. Claims: 34-45

sampling latch switching off power supply during sampling and on during latching

4. Claims: 46-53

latch using two different power supply voltages during latching and output operation  $% \left( 1\right) =\left\{ 1\right\}$ 

### ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 99 40 3241

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

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